

Electronic Acknowledgement Receipt

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Title of Invention:	Phase Error Correction Circuit
First Named Inventor/Applicant Name:	Tatsuji Ishii
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement Letter	P03776801_IDS_071971-0551.pdf	86252 10ae1a933659805d8c1c3484190f04b776d2e615	no	3

Warnings:

Information:

2	NPL Documents	P03776801_NPL_071971-0551.pdf	473193 d2cab9d48e3c88362b479573773ecd5df2be8ffe	no	13
Warnings:					
Information:					
3	Foreign Reference	P03776801_REF_071971-0551.pdf	1097919 34ca65a46d05b7b85013be93007388f5e9a5435b	no	15
Warnings:					
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